

**SUBSTITUTE SPECIFICATION****METHOD OF TRANSFERRING DATA**

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**CROSS REFERENCE TO RELATED APPLICATION**

A claim of priority under 35 U.S.C. §119 is made to Japanese Patent Application No. 2003-079902, filed March 24, 2003, which is herein incorporated by reference in its entirety for all purposes.

**BACKGROUND OF THE INVENTION****FIELD OF THE INVENTION**

The present invention relates to a method of writing data to a dynamic random access memory.

**DESCRIPTION OF THE RELATED ART**

A synchronous dynamic random access memory must have a large capacity, and must be capable of high-speed operation. However, if the capacity of the memory becomes large, a load on wiring is increased. Therefore, access speed is reduced. To solve this problem, a memory device which has a plurality of memory cell blocks is developed. However, in this memory device, a size of the memory device is increased. Another memory device which has two sets of data buses and read amplifiers has been developed. In such type of memory device, each set is operated